



### 30 Year Veteran in Semiconductor Technology & Operations

|            |  |                                 |  |
|------------|--|---------------------------------|--|
| Experience | Fab Equipment                                    | Fabrication                     | Compound Semi  |
|            | Applied Materials                                | Maxim Integrated                | Power Integration  |
|            |  | Cypress                         | Transphorm   |
| Education  | Geo-Strategy                                     | Leadership                      | Technical  |
|            | PhD Defense Studies<br>King's College, London    | PLD,<br>Harvard Business School | Master's In Engineering<br>Rochester Institute Of<br>Technology  |
|            | M.A.,<br>Non-Proliferation<br>Monterey Institute | MBA,<br>Santa Clara University  | Bachelor's In Science<br>The University Of The<br>South, Sewanee |

Seasoned technologist with over twenty years of yield enhancement, technology development, operations, and business development experience in the semiconductor wafer fabrication and equipment industry. Over ten years in management at leading Integrated device manufacturers across functional environments interacting with device, integration, module, packaging and equipment teams. Broad experience in emerging technologies of wide bandgap materials-based radiation hardened power electronics devices (Gallium Nitride), advanced packaging and CMOS technology development, scale up from university lab and transfer to volume manufacturing.

### EDUCATION

- PhD in Defense Studies, Kings College, London, 2016  
Thesis Topic: Indian Nuclear Testing Decisions: A Foreign Policy Analysis Framework based Model. Supervisors: Dr. Harsh Pant and Dr. Wyn Bowen
- MA in Nonproliferation and Terrorism Studies,  
Monterey Institute of International Studies, Middlebury College, 2010
- Master's in Microelectronics Engineering, (Full funded SRC Fellowship)  
Rochester Institute of Technology, Rochester, New York, 1994
- Bachelor's in physics and Math (Cum Laude), (Full funded University Scholarship)  
The University of the South, Sewanee, Tennessee, 1993

## **Saurabh Dutta Chowdhury**

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### **ACADEMIC RESEARCH EXPERIENCE**

- University of Leicester: Honorary Visiting Fellow in the School of History, Politics and Internal Relations 2020 and 2023-26 under my PhD thesis external examiner Professor Andrew Futter to complete my book project based on my thesis
  - Prepare Book Proposal-Done in 2020 accepted by Routledge
  - Submitted two drafts between 2023 and 24 for series editor's feedback
  - Received Book Contract September 2025 with final feedback from series editor.
  - Submitted the manuscript to publisher incorporating all edits May2026.
  - Submitted British Academy International Fellowship Grant Application 2024 Indian Strategic Cultures: Indian nuclear postures.(Grant application was unsuccessful)

### **BOOK PUBLICATION**

- Indian Nuclear Testing Decisions, 1965-2014: Human Agency, Subnational Actors, and Foreign Policy (Book contract finalized on Sept 2025 and final manuscript submitted in May 2026) Expect book to go out in print by Dec 2026

### **JOURNAL PUBLICATIONS**

- Dinshaw Mistry, Saurabh Dutta Chowdhury, Internal Drivers, External Powers, and Nuclear Test Decisions: The Case of India, Journal of Global Security Studies, Volume 9, Issue 3, September 2024
- Ambassador Sune Danielson, Enrico Carish, Saurabh Dutta Chowdhury and Durra Shatout, Forward to the Past: Sanctions and Strategic Trade Control in the Age of Algorithms, Semiconductors and Artificial Intelligence” Journal of Illicit Trade, Financial Crime, and Compliance (March 2026, Vol 2).

### **DELIVERING TEACHING WORKSHOP**

- Delivered a workshop to members of India Semiconductor industry in April 2026 highlighting the risks posed to the semiconductor supply chain by strategic threat actors. The workshop was delivered in Bangalore, India funded by Bureau of Arms Control-US Dept of State. Co-organizers were SEMI India, CCSI and HSP Partners.

### **MEDIA PUBLICATIONS**

“India’s semiconductor mission might need a compass” April1,2023 The Hindu OpEd

### **REFERENCES**

- Professor Harsh Pant Kings College London Harsh.pant@kcl.ac.uk

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- Professor Andrew Futter University of Leicester [ajf57@leicester.ac.uk](mailto:ajf57@leicester.ac.uk)